	Typ	L #	Hits	S arch T xt	DBs	Tim Stamp
1	BRS	L32	26021	(antifus$6 or fus$4 or (fus$4 near2 link$4)) near4 bond$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:26
2	BRS	L33	157969	"dynamic random access memory" or DRAM or SRAM or "static random access memory" or microprocessors	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:26
3	BRS	L35	87	ASIC near4 bond$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:27
4	BRS	L36	4356882	interconnect$6 or bridg$6 or configurat$6 or customiz$6 or adapt$6 or alter$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:27
5	BRS	L38	45451	(interconnect$6 or bridg$6 or configurat$6 or customiz$6 or adapt$6 or alter$6) near4 bond$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:27

	Type	L #	Hits	Search T xt	DBs	Time Stamp
6	BRS	L39	4199	multi near4 bond$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:27
7	BRS	L43	1903	coupl$6 near8 (bond$4 adj pad$3)	USPAT	2004/06/24 14:27
8	BRS	L44	1903	(coupl$6 near8 (bond$4 adj pad$3)) same pad$4	USPAT	2004/06/24 14:27
9	BRS	L45	989	((coupl$6 near8 (bond$4 adj pad$3)) same pad$4) same connect$6	USPAT	2004/06/24 14:27
10	BRS	L47	2598	coupl$6 near8 (bond$4 adj pad$3)	USPAT; US-PGPUB	2004/06/24 14:28
11	BRS	L48	3048	(coupl$6 near8 (bond$4 adj pad$3)) same pad$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:28
12	BRS	L49	1523	((coupl$6 near8 (bond$4 adj pad$3)) same pad$4) same connect$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:28
13	BRS	L50	885	(((coupl$6 near8 (bond$4 adj pad$3)) same pad$4) same connect$6) same electrical$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:28

	Type	L #	Hits	Search T xt	DBs	Tim Stamp
14	BRS	L52	6821	(second! near8 third near16 pad$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:28
15	BRS	L54	45152	(interconnect$6 or bridg$4 or configurat$4 or customiz$4 or adapt$6 or alter$6) near4 bond$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:28
16	BRS	L56	11086	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same bond$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
17	BRS	L57	176307	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
18	BRS	L58	2431	((interconnect$6 or bridg$4 or configurat$4 or customiz$4 or adapt$6 or alter$6) near4 bond$4) and ((antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29

	Type	L #	Hits	S arch T xt	DBs	Time Stamp
19	IS&R	L62	567	(438/623).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
20	IS&R	L63	2788	(438/624).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
21	IS&R	L64	471	(438/788).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
22	IS&R	L65	290	(438/789).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
23	IS&R	L66	334	(438/790).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29

	Typ	L #	Hits	Search Text	DBs	Time Stamp
24	IS&R	L67	1438	(438/780).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
25	IS&R	L69	404	(438/781).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
26	IS&R	L70	950	(438/107).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
27	IS&R	L71	2556	(438/106).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
28	IS&R	L72	710	(438/129).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29

	Typ	L #	Hits	Search T xt	DBs	Time Stamp
29	IS&R	L73	276	(438/132).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
30	IS&R	L74	321	(438/110).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
31	BRS	L34	486	((antifus$6 or fus$4 or (fus$4 near2 link$4)) near4 bond$6) and ("dynamic random access memory" or DRAM or SRAM or "static random access memory" or microprocessors)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
32	BRS	L37	72	(ASIC near4 bond$4) and (interconnect$6 or bridg$6 or configurat$6 or customiz$6 or adapt$6 or alter$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:29
33	BRS	L40	653	(multi near4 bond$4) same (interconnect$6 or bridg$6 or configurat$6 or customiz$6 or adapt$6 or alter$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:30

	Typ	L #	Hits	Search T xt	DBs	Time Stamp
34	BRS	L41	476	ASIC same bond$4	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/24 14:30
35	BRS	L42	100	(circuit adj portion) near8 bond$6	USPAT	2004/06/24 14:30
36	BRS	L46	583	(((coupl$6 near8 (bond$4 adj pad$3)) same pad$4) same connect$6) same electrical$6	USPAT	2004/06/24 14:30
37	BRS	L51	16	((((coupl$6 near8 (bond$4 adj pad$3)) same pad$4) same connect$6) same electrical$6) same (third near2 pad$4)	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/24 14:30
38	BRS	L53	1010	((second! near8 third near16 pad$4)) same electricall$6	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/24 14:30
39	BRS	L76	228749 6	short$6	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/24 14:37

	Type	L #	Hits	S arch T xt	DBs	Time Stamp
40	BRS	L55	87	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same (ball$3 near2 bond$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:30
41	BRS	L59	315	((interconnect$6 or bridg$4 or configurat$4 or customiz$4 or adapt$6 or alter$6) near4 bond$4) same ((antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:30
42	BRS	L60	2	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same bond$4 same ASIC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:30
43	BRS	L61	46	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same bond$4 and ASIC	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:30
44	IS&R	L68	1	("6375780").PN.	USPAT	2004/06/24 14:30
45	IS&R	L75	180	(438/131).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/24 14:31

	Type	L #	Hits	S arch Text	DBs	Tim Stamp
1	BRS	L1	26021	(antifus$6 or fus$4 or (fus$4 near2 link$4)) near4 bond$6	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/25 10:37
2	BRS	L2	228749 6	short$6	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/25 10:37
3	BRS	L3	157969	"dynamic random access memory" or DRAM or SRAM or "static random access memory" or microprocessors	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/25 10:38
4	BRS	L4	2	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same bond$4 same ASIC	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/25 10:38
5	BRS	L7	46	(antifuse or antifuses or anti-fuse or anti-fuses or fuse or fuses or (fusible near2 link$6) or (fusib$6 adj link$6)) same bond$4 and ASIC	USPAT; US-PGP UB; EPO; JPO; DERWEN T; IBM_TD B	2004/06/25 10:38

	Type	L #	Hits	Search T xt	DBs	Time Stamp
6	BRS	L8	45152	(interconnect$6 or bridg$4 or configurat$4 or customiz$4 or adapt$6 or alter$6) near4 bond$4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 10:39
7	BRS	L9	889	1 same 2	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 10:40
8	BRS	L10	889	9 same (pad or bond$6)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 10:40
9	BRS	L11	8	10 and 3	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 10:40

	Type	L #	Hits	S arch Text	DBs	Time Stamp
1	BRS	L1	1171	(antifuse or anti-fuse) near2 circuit$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:50
2	BRS	L2	114614	"memory cell"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:53
3	BRS	L3	1538395	"ball bond" or bond$6 or solder$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:56
4	BRS	L4	1610906	short or shorted or shorting or (electrical$6 adj separat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:57
5	BRS	L5	90	1 and 2 and 3 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:57

	Type	L #	Hits	Search Text	DBs	Time Stamp
6	IS&R	L12	13	(("6108260") or ("6088282") or ("5345110") or ("5331196") or ("5324681") or ("5241496") or ("5110754")).PN.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 13:08
7	BRS	L14	145	1 and 3 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 13:08
8	IS&R	L15	427	(365/96).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 13:10
9	IS&R	L18	923	(365/225.7).CCLS.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 14:16

	Type	L #	Hits	Search Text	DBs	Tim Stamp
1	BRS	L1	1171	(antifuse or anti-fuse) near2 circuit$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:50
2	BRS	L2	114614	"memory cell"	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:53
3	BRS	L3	1538395	"ball bond" or bond$6 or solder$6	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:56
4	BRS	L4	1610906	short or shorted or shorting or (electrical$6 adj separat$4)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:57
5	BRS	L5	90	1 and 2 and 3 and 4	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/06/25 11:57